### PATENT COOPERATION TREATY

# PCT

Translation

## INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference FIN 506 PCT	FOR FURTHER A	CTION	See Form PCT/IPEA/416				
International application No. International filing de		te (day/month/year)	Priority date (day/month/year)				
PCT/DE2004/001360 28.06.200		4	28.07.2003				
International Patent Classification (	PC) or national classification and	IPC					
H01L23/538, H01L21/60							
Applicant INFINEON TECHNOLOGIES AG							
<ol> <li>This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36.</li> </ol>							
2. This REPORT consists of	2. This REPORT consists of a total of sheets, including this cover sheet.						
3. This report is also accomp	anied by ANNEXES, comprising:						
a. (sent to the ap	a. (sent to the applicant and to the International Bureau) a total of sheets, as follows:						
sheets of the description, claims and/or drawings which have been amended and are the basis for this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).							
sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the Supplemental Box.							
	ternational Bureau only) a total of	(indicate type and number	r of electronic carrier(s))				
o (sent to the th	crimional Dureau omy, a total of						
, containing a sequence listing and/or tables related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section 802 of the Administrative Instructions).							
4. This report contains indic	ations relating to the following iter	ns:					
Box No. I	Basis of the report						
Box No. II	Priority						
Box No. III	Non-establishment of opinion with	regard to novelty, invent	ive step and industrial applicability				
	Lack of unity of invention	-					
	D A A A A A A A A A A A A A A A A A A A						
Box No. VI	Certain documents cited						
Box No. VII							
Box No. VIII	Box No. VIII Certain observations on the international application						
Date of submission of the demand		Date of completion of the	is report				
		*************	•				
Name and mailing address of the IPEA/EP		Authorized officer					
Engineila No		Telephone No.					

#### INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
PCT/DE2004/001360

Box	No. I	Basis of the report		
1.	With regard	to the language, this report is based on the internation ander this item.	nal application in the language in which	it was filed, unless otherwise
	which	report is based on translations from the original language is the language of a translation furnished for the purpointernational search (Rule 12.3 and 23.1(b)) publication of the international application (Rule 12.4) international preliminary examination (Rule 55.2 and/4).	oses of:	,
2.	receiving Conthis report) the in	ternational application as originally filed/furnished	e referred to in this report as "original.	ly filed" and are not annexed to
	pages			as originally filed/furnished
	pages			
	pages	*	received by this Authority on	
	the ci	aims:		
	nos.	1-6		as originally filed/furnished
	nos.*		as amended (together with	any statement) under Article 19
	nos.*		received by this Authority on	,,
	nos.*		received by this Authority on	
	the d	rawings:		
	sheet			as originally filed/furnished
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	sheet			
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		uence listing and/or any related table(s) – see Supplem	ental Box Relating to Sequence Listing.	
3.	☐ The	amendments have resulted in the cancellation of:		
	니	the description, pages		
	닐	the claims, nos.	——————————————————————————————————————	
		the drawings, sheets/figs		
İ		the sequence listing (specify):		
		any table(s) related to sequence listing (specify):		
4.		report has been established as if (some of) the amend have been considered to go beyond the disclosure as fi		
		the description, pages		
		the drawings, sheets/figs		
	一	the sequence listing (specify):		
*	If item 4 a	pplies, some or all of those sheets may be marked "sup		

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Box No. V		Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement				
1.	Statement					
	Novelty	(N)	Claims _	1-6	_ YES	
			Claims _		_ мо	
	Inventiv	Inventive step (IS)		1-6	YES	
	Industri	al applicability (IA)	Claims	1-6	YES	
			Claims _			
2.	Citations ar	nd explanations (Rule	70.7)			
	1.	This repo	ort mak	kes reference to the following		
		documents	s:			
	D1:	EP-A-0 27	73 703	(GEN ELECTRIC), 6 July 1988 (1988-		
		07-06)				
	D2:	US-A-3 65	50 796	(BUSH ERIC LANGLEY ET AL), 21 March		
		1972 (197	72-03-2	21)		
	D3:	EP-A-0 46	55 138	(GEN ELECTRIC), 8 January 1992		
		(1992-01-	-08)			
	2.	Claim 2	is unc	lear (PCT Article 6) because,		
		although	it des	scribes the optical detection of		
		semicond	uctor	chip positioning errors, it does not		
		indicate	when,	how and for what it is used.		
	ε.	The prese	ent ap;	plication appears to most the		
		requireme	ents o	f PCT Article 33(2) for novelty and		
		of PCT A	rticle	33(3) for inventive step:		
	3.1	The appl	icatio	n relates to processes for providing		
		intercon	nectio	ns on panels with chip arrays, as		
		describe	d in i	ndependent claims 1 and 2.		

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

- 3.2 The advantage of the invention is that, in spite of unavoidable chip positioning errors, the outer contacts are arranged in a totally uniform manner at the expected positions. This is achieved by defining the outer contacts separately (together with large part of the connection lines in claim 2) with a photolithographic mask which extends over the entire panel. The connection lines (or their ends in claim 2) are then successively defined by means of a second photolithographic mask having a uniform pattern (by means of short individual laser lithography in claim 2), taking into account the previously sensed chip positioning errors. Another advantage of the invention is that inaccuracies during the production of the connection lines between the chips and outer contact surfaces can be kept small. This is the result of the elimination (to a large extent, in claim 2) of direct laser structuring of the connection lines (positiondependent guidance of the lithographic laser).
- 3.3 The closest prior art, document D1 (see claims 1 and 10) discloses a process for providing interconnections on panels having semiconductor chips arranged in rows and columns (see figure 7). The interconnection lines between the individual chips are adapted during their production to previously sensed chip positioning errors. The interconnection lines are produced entirely by direct laser structuring. D1 does not mention the use of a photolithographic mask for defining outer

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citations and explanations supporting such statement contact surfaces, and even leads a person skilled in the art away from the elimination or reduction of laser lithography by the use of photolithographic masks (see D1, page 5, lines 58-60).

Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;

- 3.4 Document D2 (see the entire document) describes the production of photolithographic masks. Since D1 (see page 5, lines 58-60) discourages the use of photolithographic masks, a person skilled in the art would have no reason to combine D1 with D2 in whatever form.
- 3.5 Document D3 only gives general background information.
- 4. Consequently, the dependent claims also meet the requirements of PCT Article 33(2) and 33(3).